|             | Form PTO-1449 (Modified)          | Atty Docket No. | Serial No.    |
|-------------|-----------------------------------|-----------------|---------------|
|             | LIST OF PATENTS AND PUBLICATIONS  | AMDSP0499US     | 09/824,933    |
| i           | FOR APPLICANT'S                   | Applicant: Lin  |               |
|             | TINFORMATION DISCLOSURE STATEMENT |                 |               |
|             | (Use several sheets if necessary) | Filing Date     | Group         |
| 00          | 0 4 2002                          | April 3, 2001   | Not yet known |
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**U.S. PATENT DOCUMENTS** 

| Examiner<br>Initial | Document Number | Date     | Name            | Class | Sub-<br>class | Filing Date if<br>Appropriate |
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| K.N.   | Wolf et al.; "Silicon Processing for the VLSI Era"; Crystalline Defects, Thermal Processing and Gettering; Basic Gettering Principles; Volume 1: Process Technology; pgs. 63-70. |                 |  |
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| Form PTO-1449 (Modified)                          | Atty Docket No.           | Serial No.          |  |
|---|---------------------------|---------------------|--|
| LIST OF PATENTS AND DURI ICATIONS                 | AMDSP0448US               | 09/824,933          |  |
| FOR APPLICANT'S  INFORMATION DISCLOSURE STATEMENT | Applicant: Lin            |                     |  |
| (Use several sheets if necessary)                 | Filing Date April 3, 2001 | Group Not yet known |  |

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